## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:		§	Art Unit:	2186
	Ramesh V. Peri et al.	§		
		§	Examiner:	Pierre Miche Bataille
Serial No.:	10/717,085	§		
E11 1	1 10 2002	Š	Conf. No.:	7032
Filed:	November 19, 2003	8	Docket:	ITL.1059US
For:	A	8	Docket:	P17918
POT:	Accessing Data From	8		P1/918
	Different Memory Locations	§		
	in the Same Cycle	§.	Assignee:	Intel Corporation

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed September 18, 2008, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made to the claims.

Date of Deposit: February 4, 2009
I finereby certify that this correspondence is being electronically transmitted on the date indicated above.

Cynthia L. Hayden